[**Programmable Communication Group**](https://sites.google.com/a/temple.edu/programmable-communication-group/)

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| Date | Friday, November 08, 2013 | | |
| Advisor | Dr. Silage | | |
| Members | Cedric Destin | Brandon Keith | Brian Thibodeau |

Headline: It is crunch time! Keep in mind that the Preliminary Prototype is due 11/22 and the Design Document is due 12/02.

Topics to discuss (with Silage)

* Provide updated design document draft with updated sections 1.1 (Overall objective) and 1.2 (Historical and Economic Perspective).
* Analytical expressions for PLL?

Topics to discuss (among SD team and Silage, if necessary)

* Scheduling/tasking for the remainder of SD1.

Dr. Silage feedback

Topics to discuss in next SD meeting

* Provide updated design document draft with updated sections 1.3-1.5.

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| **Engineer** | **Status** |
| Brian Thibodeau |  |
| Cedric Destin | * Implementing BFSK demodulation using PLL * Implemented BFSK demodulation using optimal non-coherent receiver |
| Brandon Keith | * Updating sections 1.3 – 1.5 of DD draft * Researching FEC methods * Researching interleaving methods |